

REMARKS

Claims 1, 4-11, and 17-27 are pending in the application. Claims 1 and 17-27 have been amended. No new matter has been added. Reconsideration is respectfully requested in view of the amendments to the claims and the following remarks.

I. The § 102 Rejections

Claims 1 and 17 were rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 5,559,954 ("Sadoka").

Claims 1, 4-11, and 17-27 were rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent. 5,914,729 ("Lipincott").

Applicant respectfully traverses the rejections.

Claim 1 recites providing a first frame buffer configured to support a first format compatible with a format associated with an application program, and providing a second frame buffer that is separate from the first frame buffer, in which the second frame buffer is configured to support a second format compatible with a format associated with an output device. The method further includes transforming data provided by the application program from the first format supported by the first frame buffer to the second format supported by the second frame buffer for output on the output device.

A. Sadoka Fails To Disclose Providing a Second Frame Buffer That Is Separate From A First Frame Buffer

Sadoka discloses a computer system that is capable of executing a plurality of application programs and generating multi-format pixels for display on a computer display monitor (see Abstract). In particular, referring to FIG. 2 of Sadoka, the computer system includes a host processor system 18, a multi-format frame buffer 26, a RAM DAC 28, and a

computer display monitor 30. During a display period of the display monitor 30, multi-format pixels are clocked out of the multi-format frame buffer 26 and transferred to the RAM DAC 28. The RAM DAC 28 converts each multi-format pixel to a RGB definition compatible with the display monitor 30 (col. 5, ll. 17-33).

While Sadoka discloses converting multi-format pixels to a RGB definition compatible with a display monitor, Sadoka's system includes only a single frame buffer – i.e., the multi-format frame buffer 26. Sadoka fails to disclose that the RAM DAC 28 is a frame buffer. In general, a frame buffer is a memory that stores one or more frames of video information for display on a screen, and Sadoka fails to disclose that the RAM DAC 28 can store a frame of video information.

Applicant respectfully submits, therefore, that claim 1 is allowable over Sadoka.

Claim 17 includes limitations similar to claim 1 and is also allowable over Sadoka for reasons corresponding to those set forth with respect to claim 1.

*B. Lipincott Fails To Disclose Transforming Data Provided By An Application Program
From a First Format Supported By a First Frame Buffer To a Second Format
Supported By a Second Frame Buffer For Output On An Output Device*

Lipincott discloses a visual frame buffer architecture for processing visual data (see Abstract). More specifically, Lipincott's visual frame buffer architecture includes a graphics controller, a first storage means that stores graphics data, and a second storage means that stores video data (col. 5, ll. 55-61). In operation, the graphics controller generates a merged pixel stream from the visual data stored in the first and second storage means (i.e., RAM BANK 0, RAM BANK 1) (col. 5, ll. 4-7; FIG. 2).

While Lipincott discloses generating a *merged* pixel stream from data stored in RAM BANK 0, RAM BANK 1, Lipincott nevertheless fails to disclose transforming data provided

Attorney Docket: P1916C/526C

by the application program from the first format supported by the first frame buffer to the second format supported by the second frame buffer for output on the output device. That is, Lipincott fails to disclose that the graphics data stores in the first storage means is transformed into video data, or that the video data stored in the second storage means is transformed into graphics data. Rather, Lipincott discloses only generating a merged pixel stream based on the data stored in the first and second storage means.

Applicant respectfully submits that claims 1 and 17, and the claims that depend therefrom, are allowable over Lipincott.

Applicant submits that claims 1, 4-11, and 17-27 are allowable over the references cited above, and are in condition for allowance. Should any unresolved issues remain, the Examiner is invited to call the undersigned at the telephone number indicated below.

Respectfully submitted,
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